TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (MACH II  $\pi$ -MOS V)

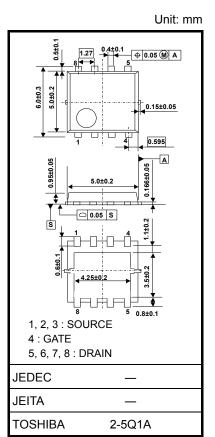
# **TPCA8010-H**

High-Speed Switching Applications Switching Regulator Applications DC-DC Converter Applications

- Small footprint due to a small and thin package
- High-speed switching
- Small gate charge: QSW = 3.7 nC (typ.)
- Low drain-source ON-resistance: RDS (ON) =  $0.38\Omega$  (typ.)
- High forward transfer admittance:  $|Y_{fs}| = 3.9S$  (typ.)
- Low leakage current:  $I_{DSS} = 100 \mu A (max) (V_{DS} = 200V)$
- Enhancement mode:  $V_{th} = 2.0 \text{ to } 4.0 \text{ V (VDS} = 10 \text{ V, ID} = 1 \text{ mA)}$

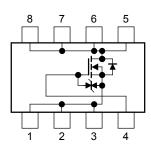
### **Absolute Maximum Ratings (Ta = 25°C)**

Characte	eristic	Symbol	Rating	Unit
Drain-source voltage		$V_{DSS}$	200	V
Drain-gate voltage (R	$k_{GS} = 20 \text{ k}\Omega$	$V_{DGR}$	200	V
Gate-source voltage		V <sub>GSS</sub>	±20	V
Drain current	DC (Note 1)	I <sub>D</sub>	5.5	А
Dialii cuileiit	Pulsed (Note 1)	I <sub>DP</sub>	11	A
Drain power dissipati	on (Tc=25°C)	PD	45	W
Drain power dissipati	on (t = 10 s) (Note 2a)	P <sub>D</sub>	2.8	W
Drain power dissipati	on (t = 10 s) (Note 2b)	P <sub>D</sub>	1.6	W
Single-pulse avalance	ne energy (Note 3)	E <sub>AS</sub>	19	mJ
Avalanche current		I <sub>AR</sub>	5.5	Α
Repetitive avalanche	energy c=25°C) (Note 4)	E <sub>AR</sub>	1.5	mJ
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature	range	T <sub>stg</sub>	-55 to 150	°C



Weight: 0.069 g (typ.)

## **Circuit Configuration**



Note: For Notes 1 to 4, refer to the next page.

Using continuously under heavy loads (e.g. the application of high

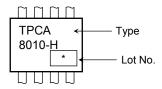
temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Handle with care.

#### **Thermal Characteristics**

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case (Tc=25°C)	R <sub>th (ch-c)</sub>	2.78	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2a)	R <sub>th (ch-a)</sub>	44.6	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	R <sub>th (ch-a)</sub>	78.1	°C/W

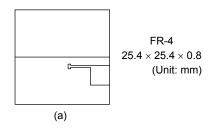
### Marking (Note 5)

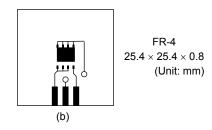


Note 1: The channel temperature should not exceed 150°C during use.

Note 2: (a) Device mounted on a glass-epoxy board (a)

(b) Device mounted on a glass-epoxy board (b)

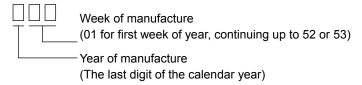




Note 3:  $V_{DD} = 50$  V,  $T_{ch} = 25^{\circ}C$  (initial), L = 1mH,  $R_G = 25~\Omega$ ,  $I_{AR} = 5.5~A$ 

Note 4: Repetitive rating: pulse width limited by max channel temperature

Note 5: \* Weekly code: (Three digits)



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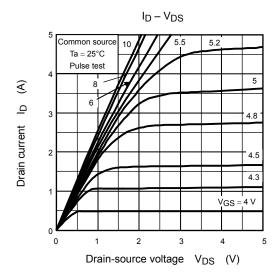
# **Electrical Characteristics (Ta = 25°C)**

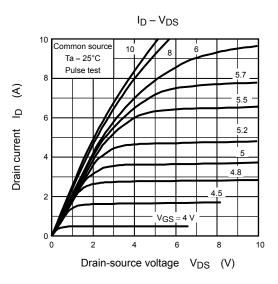
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μА
Drain cutoff curre	nt	I <sub>DSS</sub>	V <sub>DS</sub> = 200 V, V <sub>GS</sub> = 0 V	_	100		μΑ
		V <sub>(BR) DSS</sub>	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	200	_	_	
Drain-source breakdown voltage		V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -5 \text{ V}$	200	_	_	V
			$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	150	_	_	
Gate threshold vo	oltage	V <sub>th</sub>	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$	2.0	_	4.0	V
Drain-source ON-resistance		R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 2.7 A	_	0.38	0.45	Ω
Forward transfer admittance		Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 2.7 A	1.8	3.9	_	S
Input capacitance		C <sub>iss</sub>		_	600	_	pF
Reverse transfer	Reverse transfer capacitance		V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	20	_	
Output capacitance		C <sub>oss</sub>		_	220	_	
Switching time	Rise time	t <sub>r</sub>	V <sub>GS</sub> 10 V	_	8	_	ns
	Turn-ON time	t <sub>on</sub>		_	17	_	
	Fall time	tf		_	13	_	
	Turn-OFF time	t <sub>off</sub>	Duty ≤ 1%, t <sub>w</sub> = 10 μs	_	70	_	
Total gate charge (gate-source plus gate-drain)		Qg		_	10	_	
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 160 \text{ V}, V_{GS} = 10 \text{ V},$		7.6		nC
Gate-drain ("miller") charge		Q <sub>gd</sub>	I <sub>D</sub> = 5.5 A	_	2.4	_	
Gate switch charge		Q <sub>sw</sub>		_	3.7	_	

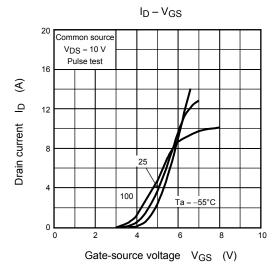
## **Source-Drain Ratings and Characteristics (Ta = 25°C)**

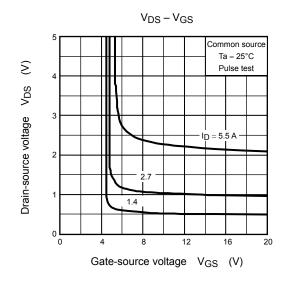
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit	
Drain reverse current	Pulse	(Note 1)	I <sub>DRP</sub>	_	_	_	11	Α
Forward voltage (diode)			V <sub>DSF</sub>	I <sub>DR</sub> = 5.5 A, V <sub>GS</sub> = 0 V	_	_	-2.0	V

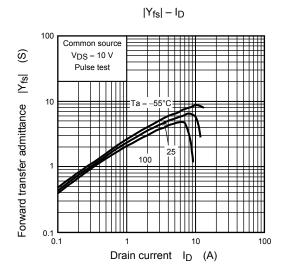
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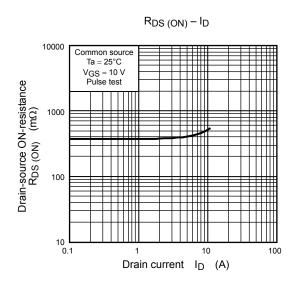


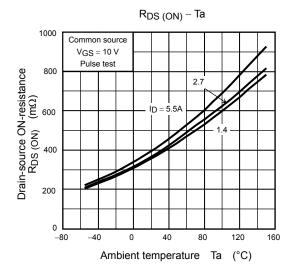


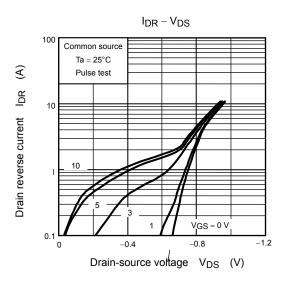


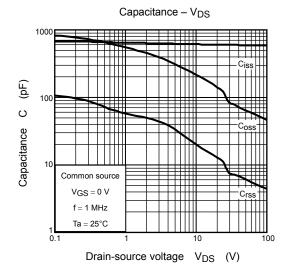


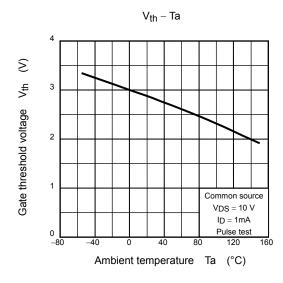


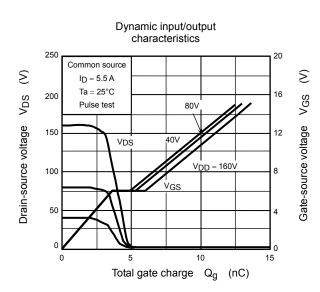


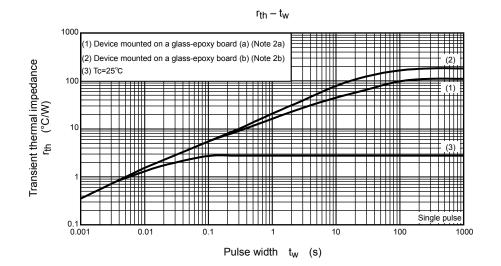


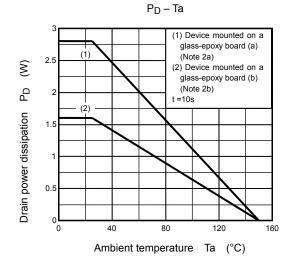


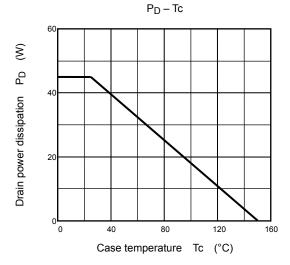


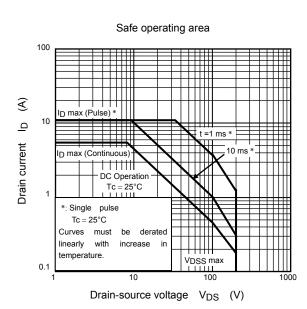












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